Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	0	702/185 and (margin with test$3) and (fault with bypass) and (mask$3 with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 13:47
S2	1	702/185 and (margin with test$3) and (fault with bypass)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 13:47
S3	0	(margin with test$3) and (fault with bypass) and (mask$3 with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 15:04
S4	1	(margin with test$3) and fault and bypass and (mask$3 with fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 15:04
S5	4	("6351827" "6496790").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 13:52
S6	0	(("6351827" "6496790").pn.) and (margin with test$3) and (fault with bypass)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 13:55
S7	0	(("6351827" "6496790").pn.) and (margin with test$3) and fault and bypass	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 13:55
S8	1	(margin with test$3) and (fault with bypass) and (fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 19:12
S9	0	(margin with test$3) and (fault with bypass) and (inter-integrated with circuit with bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 15:07
S10	0	(margin with test$3) and (inter-integrated with circuit with bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 15:08

S11	3	((inter-integrated with circuit with bus)) and (fault with bypass) and (fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 15:08
S12	12	(margin with test$3) and (interrupt with signal) and (fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/07 16:32
S13	0	(power with control$3) same communication same (fault with bypass) same transmit same (fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 14:50
S14	1	(power with control$3) same (fault with bypass) same transmit same (fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 14:52
S15	0	((power with control$3) same (fault with bypass)) and (absence with margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 14:54
S16	0	(power with control$3) and (fault with bypass) and (absence with margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 15:01
S17	0	(power with control$3) and (absence with margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 14:55
S18	0	(power with control$3) and (fault with bypass) and (margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 14:55
S19	0	(power with control$3) same (fault with signal) same (margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 15:04
S20	0	(power with rail) same (interrupt with signal) same (margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 15:05

S21	0	((power with rail) same (interrupt with signal)) and (margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 15:12
S22	0	(temperature with diode) same (supply with temperatue) same monitor$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/08 15:13
S23	0	(margin with test$3) and (fault with signal) and BMC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 11:05
S24	50	(margin with test$3) and (fault$1 with signal) and (indicat$3 with fault$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 11:06
S25	0	((margin with test$3) and (fault$1 with signal) and (indicat$3 with fault$1)) and (fault with bypass)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 11:07
S26	0	((margin with test$3) and (fault$1 with signal) and (indicat$3 with fault$1)) and (fault same bypass)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 11:07
S27	14	((margin with test$3) and (fault$1 with signal) and (indicat$3 with fault$1)) and fault and bypass	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:13
S29	10	(margin with test$3) and (mask$3 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 13:22
S30	5	S29 and (indicat$4 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:11
S31	0	(margin with test$3) and (indicat$4 with signal with lack$3 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 14:28

S32	2	(margin with test$3) and (signal with lack$3 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 14:27
S33	1	(margin with test$3) and (indicat$4 with lack$3 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 14:36
S34	0	(power with control$3) same ((absence or lack) with margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:06
S35	0	(power with control$3) and (margin with test$3) and ("not" with operat$3 with margin with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:07
S36	0	(power with control$3) and (margin with test$3) and (absence with margin with mode)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:09
S37	0	(power with control$3) and (absence with margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:09
S38	10	(margin with test$3) and (mask$3 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:11
S39	0	S38 and (indicat$4 with pass with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:11
S40	1	(margin with test$3) and (fault$1 with signal) and (indicat$3 with pass with fault$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:36
S41	0	"6667917".pn. and (margin with test$3) and (indicat$3 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:16

S42	2	"6667917".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:16
S43	18	(margin with test$3) and (fault$1 with signal) and (interrupt$3 with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 15:37
S44	6	S43 and bypass	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:20
S46	0	bypass same (program$3 with logic) same fault same signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:24
S47	40	bypass same program$3 same fault same signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:22
S48	0	S47 and (mask$3 with fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:23
S49	18	S47 and (fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:23
S50	0	S49 and (margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:23
S51	10	(program$3 with logic) and (mask$3 with fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:28
S52	3	S51 and bypass	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 16:37

S53	0	(Baseboard with Management with Controller) and (margin with test$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 17:14
S54	51	(Baseboard with Management with Controller)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 18:24
S55	2	S54 and test$3 and (indicat$4 with fault)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 17:15
S56	8	(Baseboard with Management with Controller) and (Inter-Integrated with Circuit with bus)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 18:29
S57	3	S56 and test$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 18:30
S58	1	S57 and (fault or error)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 18:29
S59	1	(margin with test$3) and ((computer CPU microcomputer microprocessor processor) with (record$3 or monitor$3) with fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 19:09
S60	16	(margin with test$3) and ((computer CPU microcomputer microprocessor processor) with fault with signal)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 19:10
S61	8	S60 and bypass	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/11/09 19:10